



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Dougherty et al. Examiner: P. Dinh  
Serial No: 10/780,140 Group Art Unit: 2825  
Filed: February 17, 2004 Docket: YOR920030437US1  
(8728-653)  
For: METHOD FOR OPTIMIZATION OF LOGIC CIRCUITS FOR  
ROUTABILITY IMPROVEMENT

Mail Stop AF  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

RESPONSE

Sir:

This is a response to the Final Office Action mailed August 1, 2006.

OK to enter  
the response (remarks)  
PD

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to: Mail Stop AF, Commissioner for Patents, PO Box 1450, Alexandria VA, 22313-1450 on the date indicated below.

Dated: 9/21/06

  
Frank DeRosa